

FIG. 1 - System Block Diagram

The Handset Side

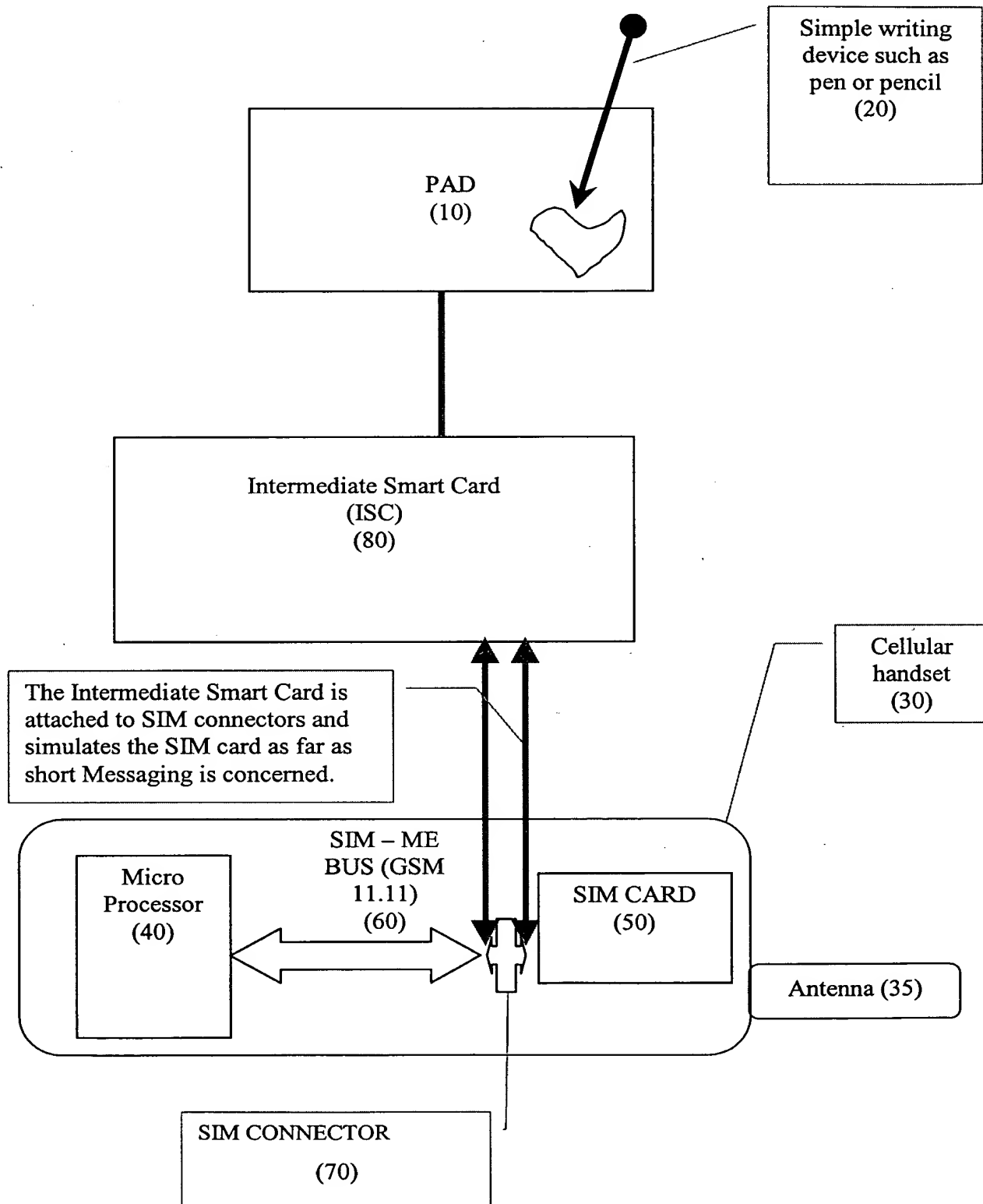
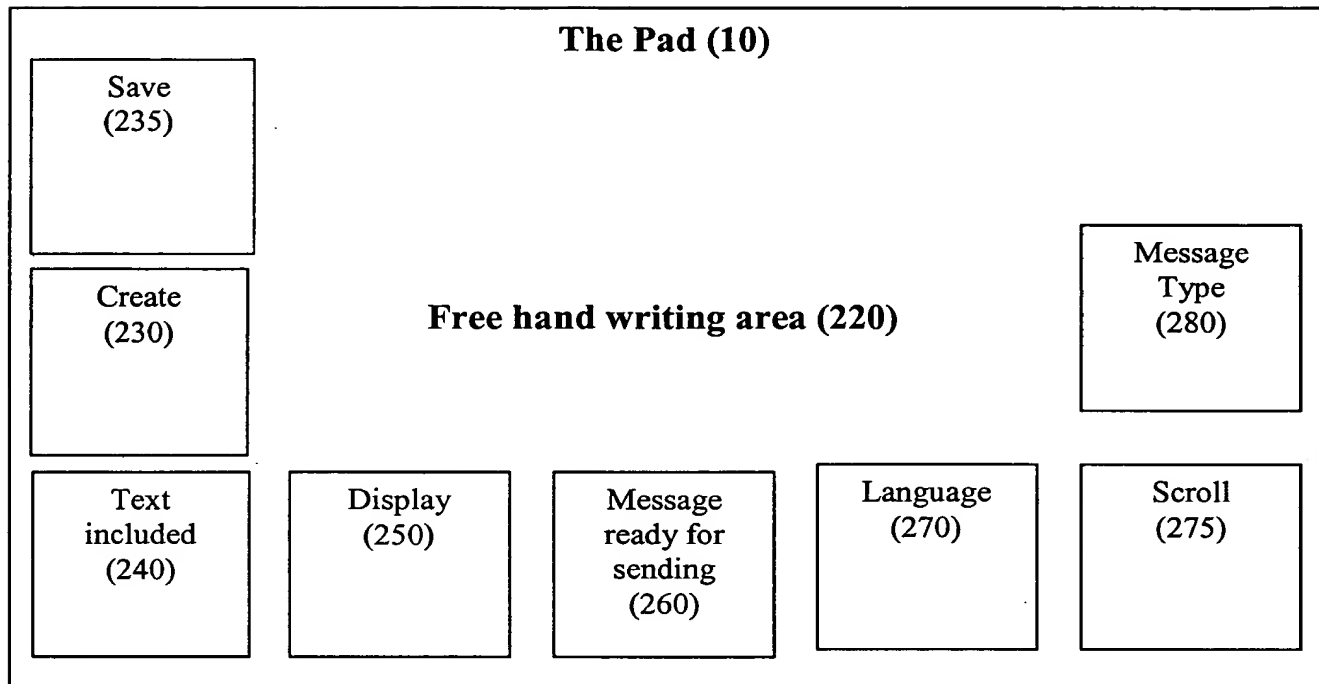


FIG. 2 - System Block Diagram

Possible structure of the Pad



The diagram shows the internal components of an Intermediate Smart Card (ISC) (80). It consists of five rectangular blocks arranged in two rows. The top row contains 'QUANTIZER (320)' and 'MEMORY (340)'. The bottom row contains 'OCR - OPTIONAL (330)', 'ENCODER (350)', and 'DECODER (360)'. All components are interconnected to a common bus system, represented by a horizontal line at the top and a vertical line on the left, which then branches out to each component block.

```
graph TD
    Bus[ ] --- Q[QUANTIZER (320)]
    Bus --- M[MEMORY (340)]
    Bus --- OCR[OCR - OPTIONAL (330)]
    Bus --- E[ENCODER (350)]
    Bus --- D[DECODER (360)]
```

Intermediate Smart Card (ISC) (80)

QUANTIZER (320)

MEMORY (340)

OCR - OPTIONAL (330)

ENCODER (350)

DECODER (360)

FIG. 4 - System Block Diagram

Integration within the Cellular Network

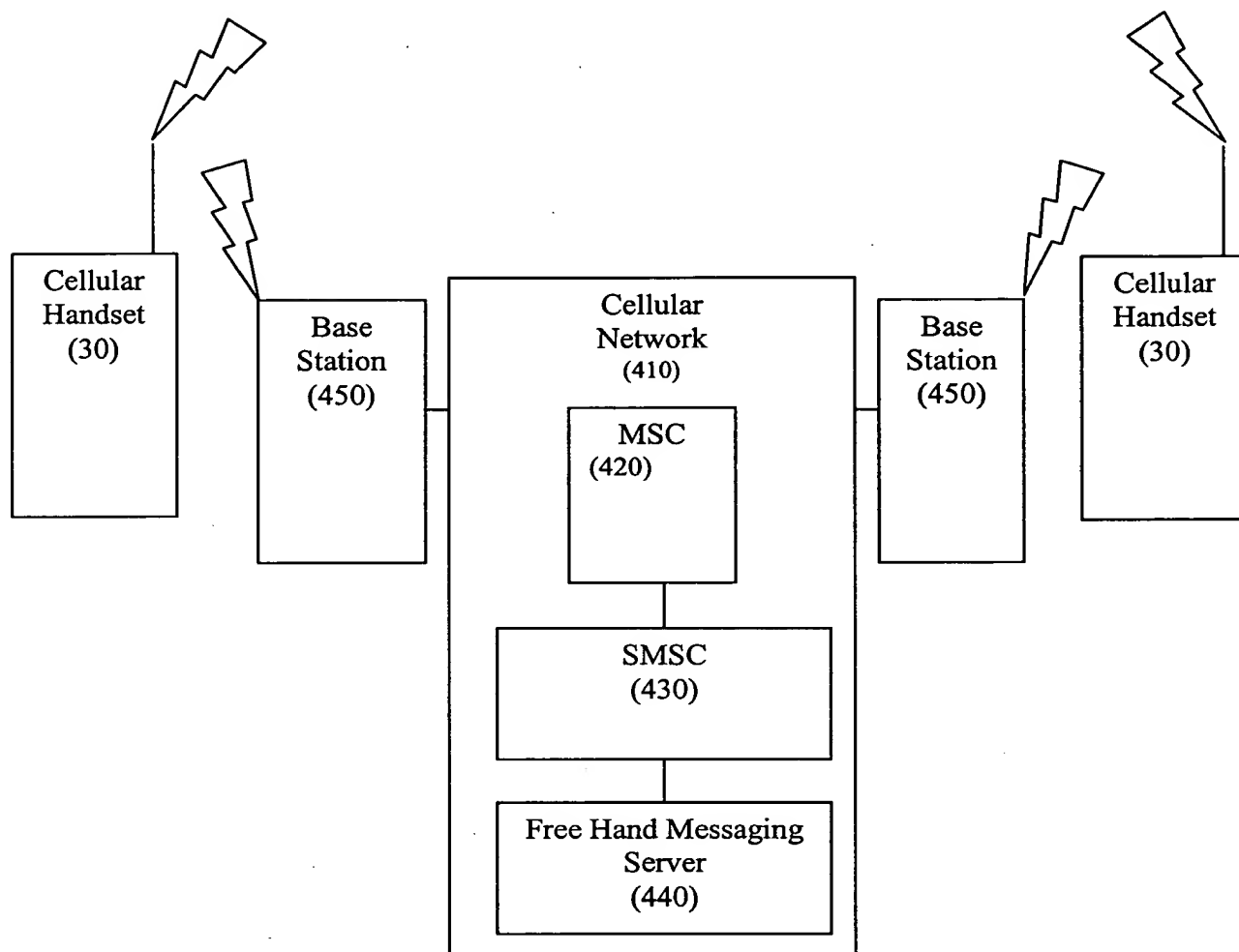


FIG. 4 - System Block Diagram

FIG. 5 - System Block Diagram

Free Hand Messaging Server - Structure

Free Hand Messaging Server (440)

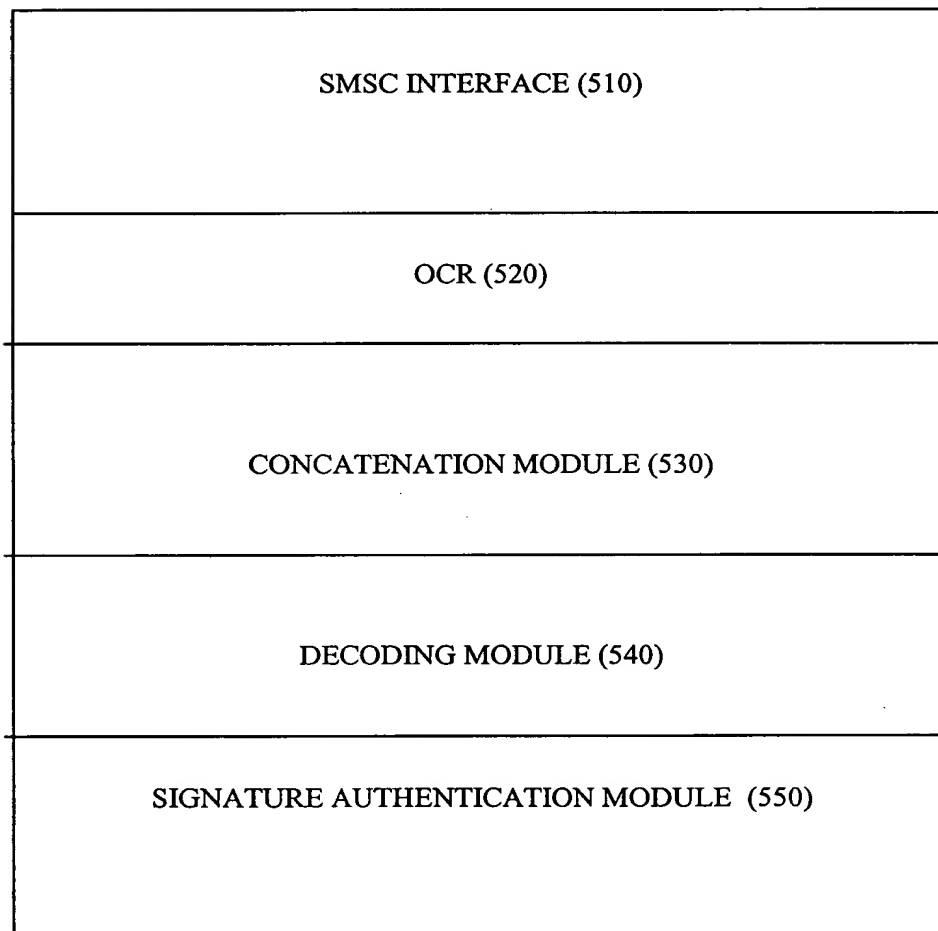
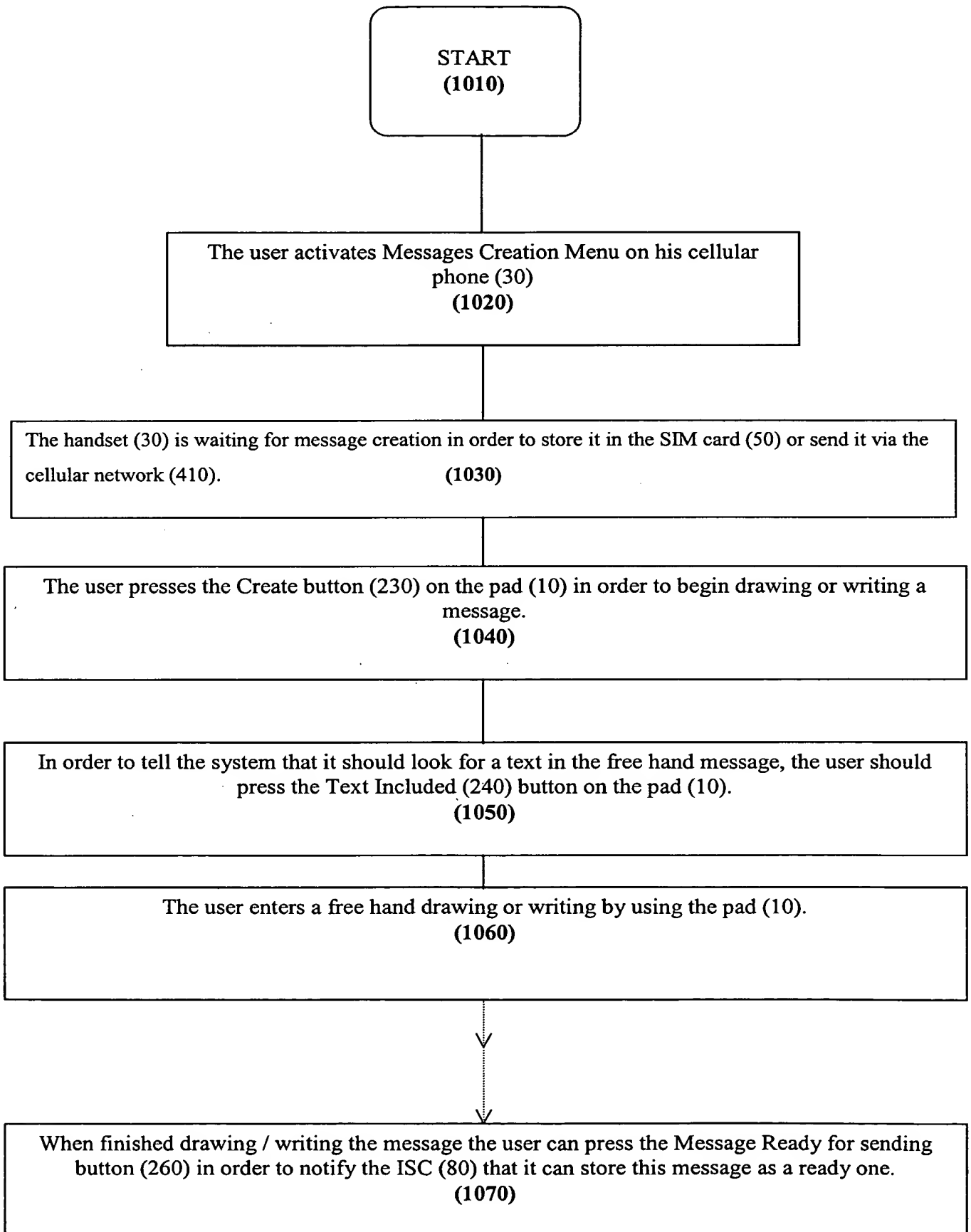


FIG. 5 - System Block Diagram

FIG. 6 - The Process – MO (Mobile Originated)



00015553 079704
102620 2351000

FIG. 6 CONT'D

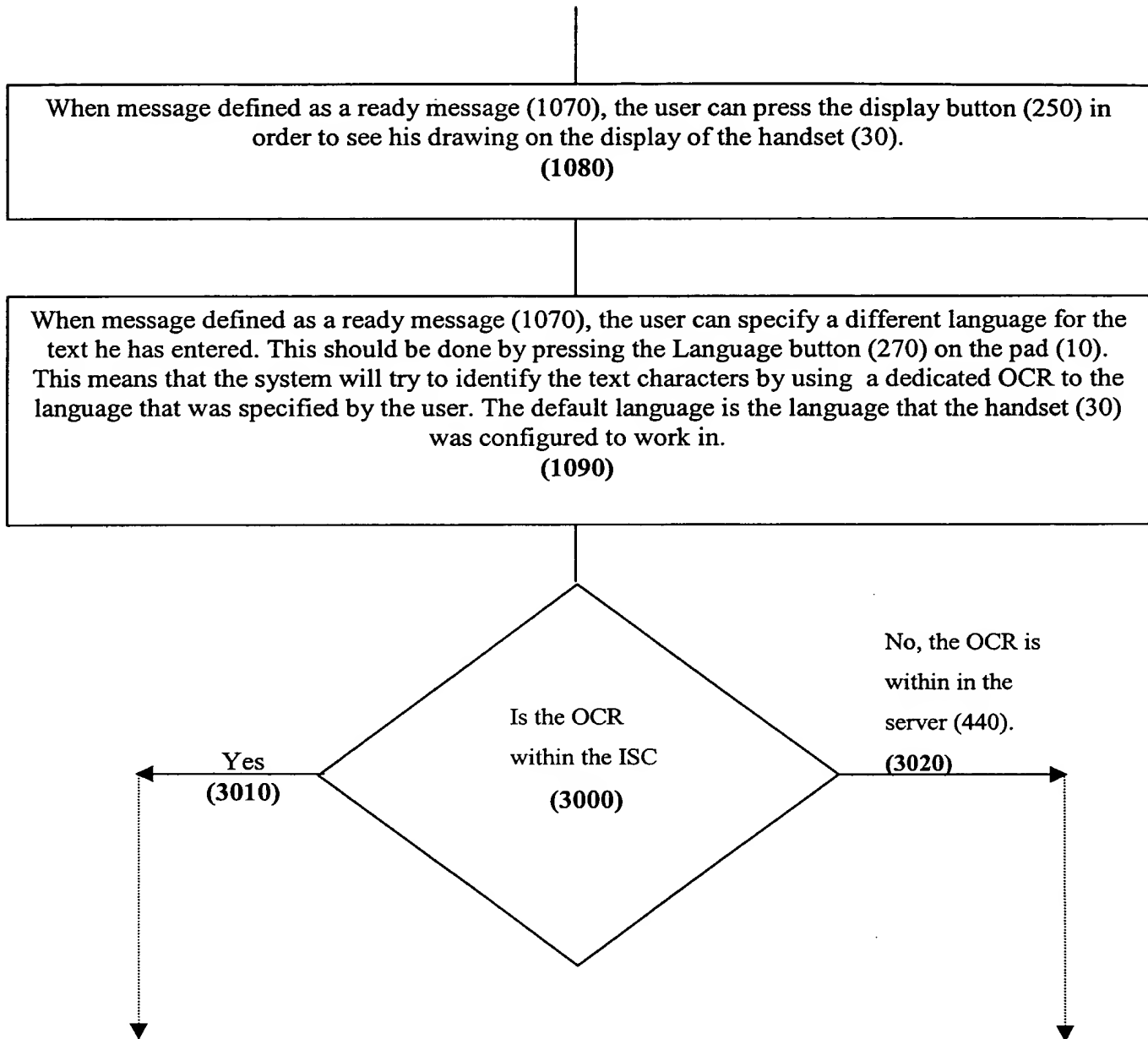


FIG. 6 CONT'D

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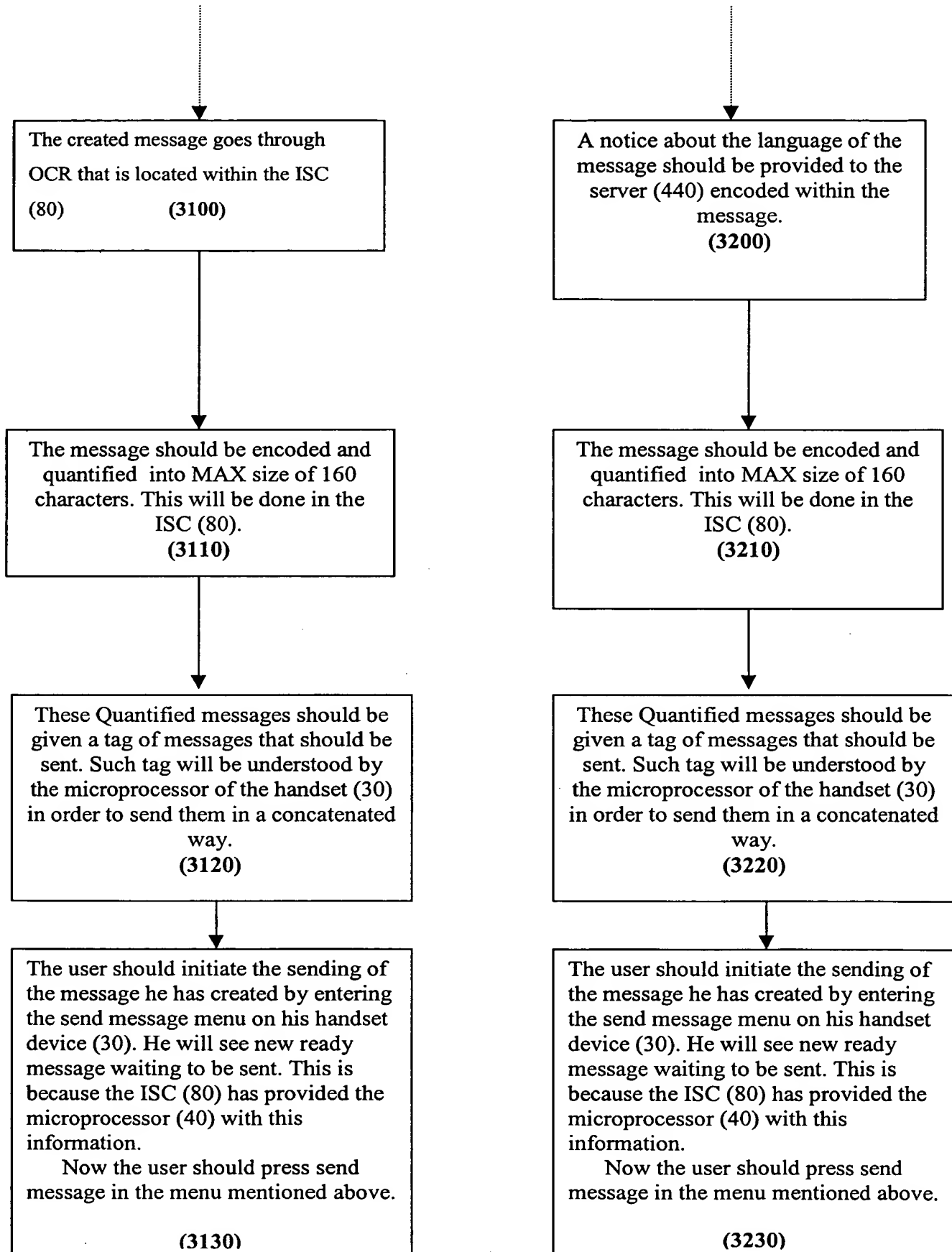
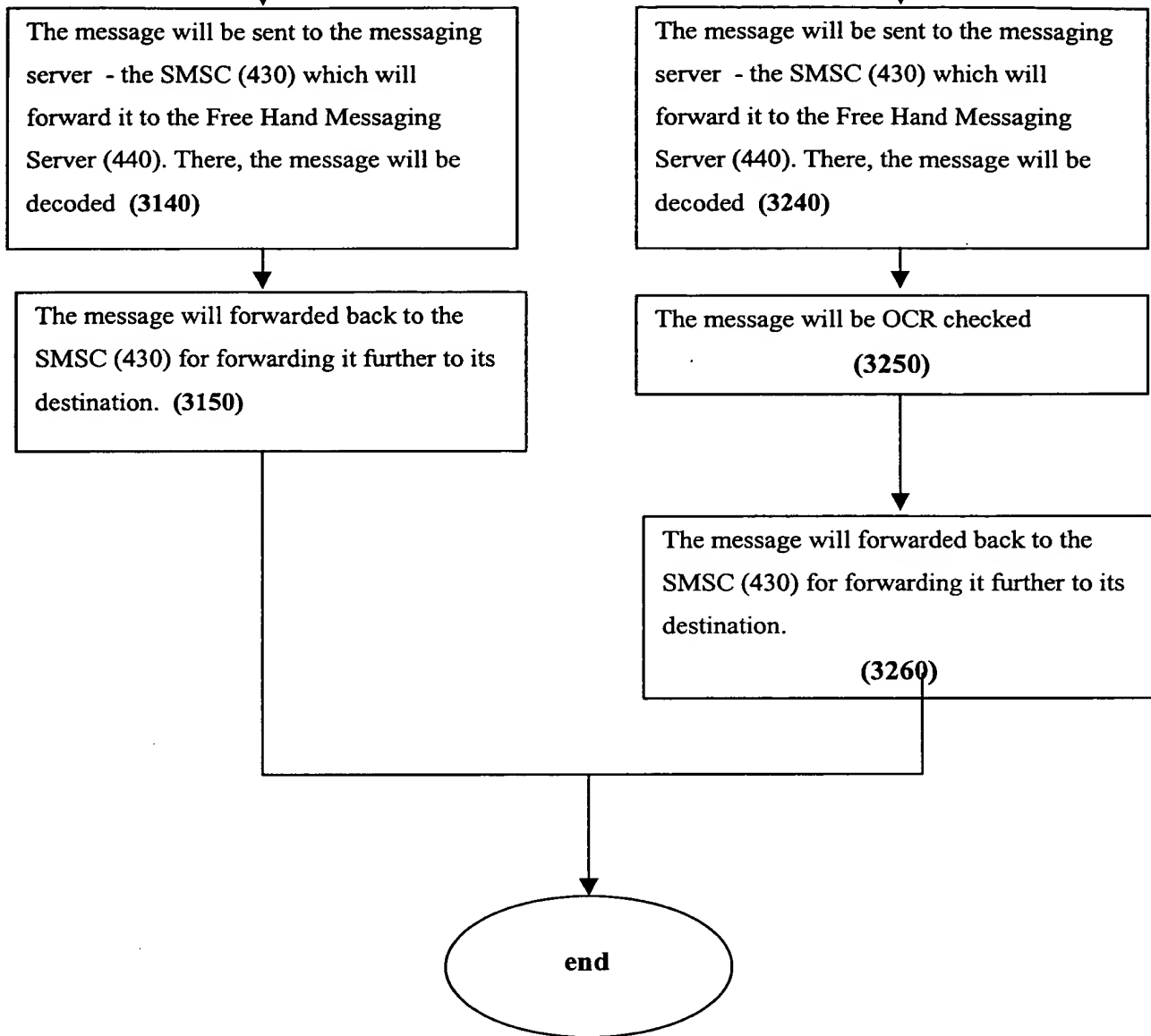
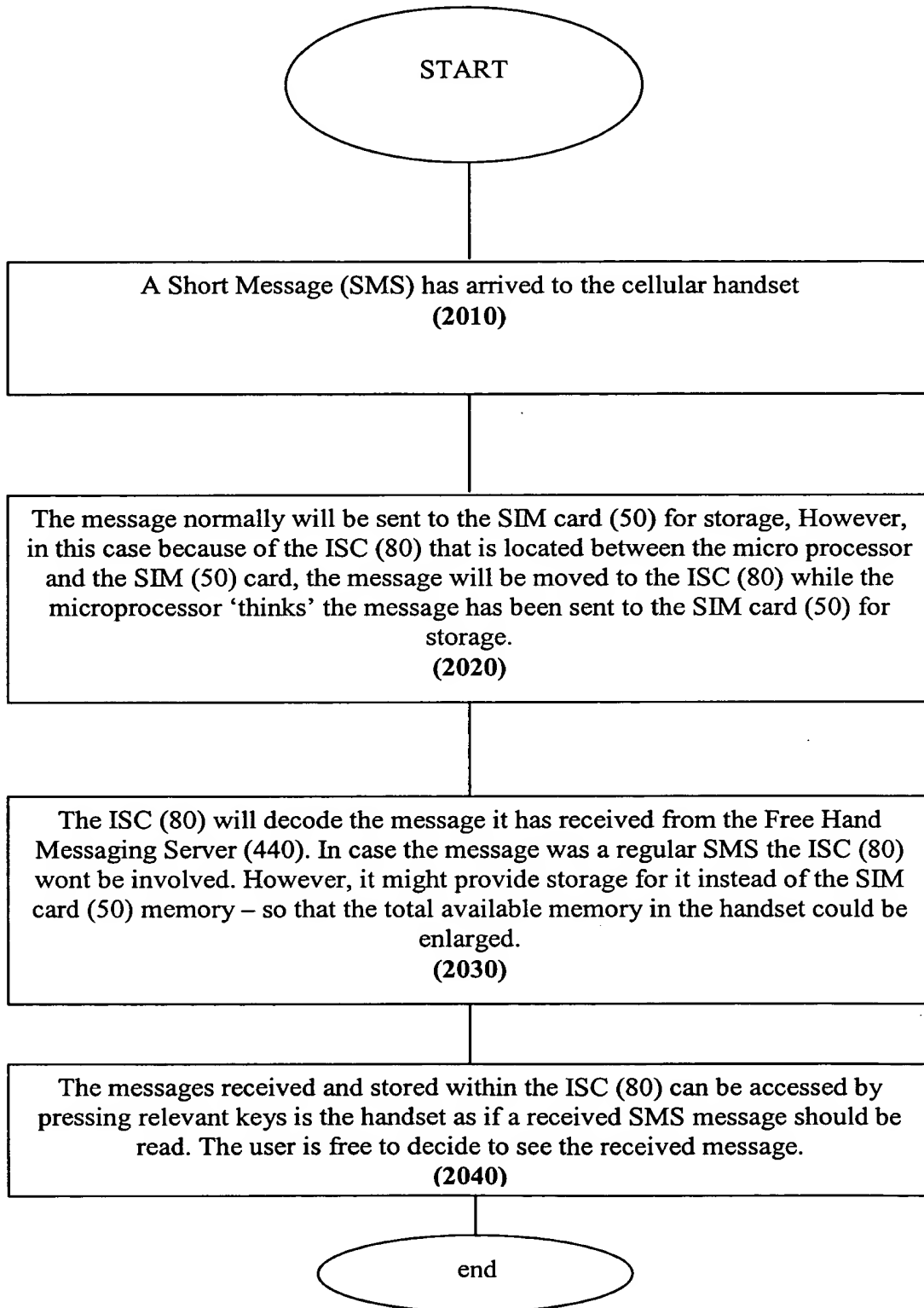


FIG. 6 CONT'D



0904563-072704
10/22/01 16:50

FIG. 7 - The Process – MT (Mobile Terminated)



001553-07290
T06220-2951650